

STANDARD EXPERIMENT DATA RECORD HEADER

ITEM	WORD	BITS	DESCRIPTION
39	8	21	<p><u>SCLK Reference Flag</u> - Status as to whether SCLK reference has been established. (Extracted from bit 5, word 21 of SDR header.)</p> <p>0=2 or more successive frames with compatible SCLK values have been encountered (or N/A). 1=No successive frames with compatible SCLK values have been encountered.</p>
40	8	22	<p><u>Parent Frame SCLK Corrected Flag</u> - Status of whether SCLK has been corrected based on extraction from higher order module. (Extracted from bit 6, word 21 of SDR header.)</p> <p>0=Parent frame SCLK not corrected. 1=Parent frame SCLK corrected.</p>
41	8	23	<p><u>SCLK Computed Flag</u> - Status of the SCLK value being computed. (Extracted from bit 7, word 21 of the SDR header.)</p> <p>0=SCLK processed as is from the S/C 1=SCLK value has been computed</p>
42	8	24-31	Spares - Binary Zeros.